

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Group Art Unit: To be Assigned  
Examiner: To be Assigned

In re PATENT APPLICATION of:

Applicant	:	Makoto TERUI et al	)	
			)	
Appln. No.	:	To be Assigned	)	
			)	
Filed	:	Concurrently (As a Division of Application No. 10/022,268, filed December 20, 2001)	)	<b>CLAIM FOR <u>PRIORITY</u></b>
			)	
For	:	SEMICONDUCTOR PACKAGE AND METHOD OF FABRICATING SAME	)	
			)	
Attorney Ref.	:	OKI 286 D1	)	
			)	
				March 29, 2004

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

It is respectfully requested that the above-identified divisional application be given the benefit under 35 U.S.C. §119 of the foreign filing date of Japanese application number 243274/, filed on August 10, 2001. A certified copy of the Japanese priority application was filed in the parent of the present application (the parent application is number 10/022,268, filed December 20, 2001) on December 20, 2001.

It is respectfully requested that fulfillment of the requirements for claiming priority be acknowledged in the next communication from the Patent and Trademark Office.

Respectfully submitted,



Allen Wood - Reg. No. 28,134  
RABIN & BERDO, P.C.  
Telephone: 202-371-8976  
Telefax: 202-408-0924  
CUSTOMER NO. 23995

FEE ENCLOSED:\$ 770  
Please charge any further  
fee to our Deposit Account  
No. 18-0002

March 29, 2004  
Date